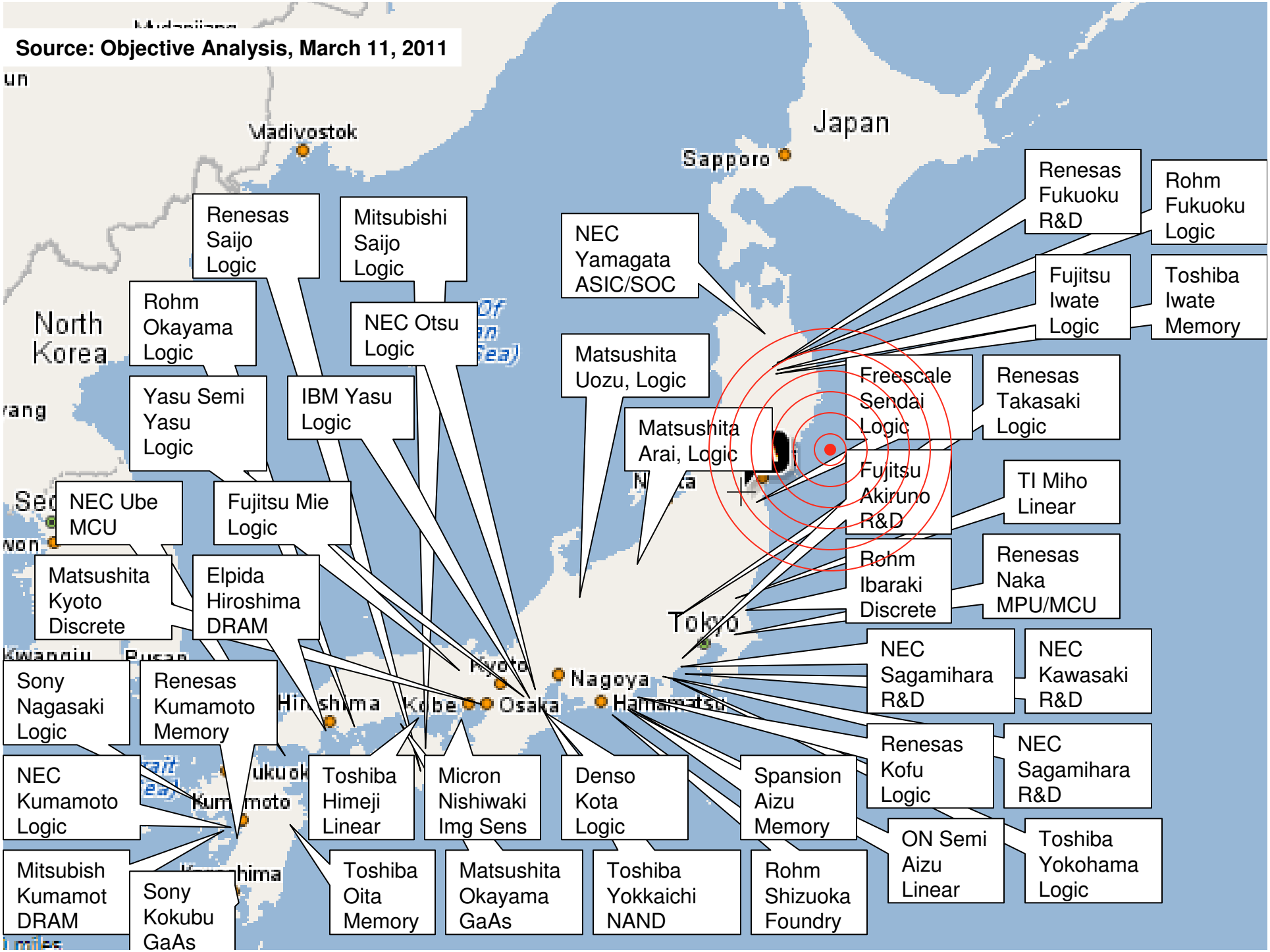
Source: Objective Analysis, March 11, 2011
Vladivostok
Japan
Sapporo
North Korea
Renesas Fukuoku R&D
Rohm Fukuoku Logic
Fujitsu Iwate Logic
Toshiba Iwate Memory
Renesas Saijo Logic
Mitsubishi Saijo Logic
NEC Yamagata ASIC/SOC
Rohm Okayama Logic
NEC Otsu Logic
Matsushita Uozu, Logic
Yasu Semi Yasu Logic
IBM Yasu Logic
Freescale Sendai Logic
Renesas Takasaki Logic
Matsushita Arai, Logic
Fujitsu Akiruno R&D
TI Miho Linear
NEC Ube MCU
Fujitsu Mie Logic
Rohm Ibaraki Discrete
Renesas Naka MPU/MCU
Matsushita Kyoto Discrete
Elpida Hiroshima DRAM
Tokyo
NEC Sagamihara R&D
NEC Kawasaki R&D
Sony Nagasaki Logic
Renesas Kumamoto Memory
Nagoya
Osaka
Renesas Kofu Logic
NEC Sagamihara R&D
NEC Kumamoto Logic
Toshiba Himeji Linear
Micron Nishiwaki Img Sens
Denso Kota Logic
Spansion Aizu Memory
ON Semi Aizu Linear
Toshiba Yokohama Logic
Mitsubish Kumamot DRAM
Sony Kokubu GaAs
Toshiba Oita Memory
Matsushita Okayama GaAs
Toshiba Yokkaichi NAND
Rohm Shizuoka Foundry